1.) Perform the Ford-Fulkerson algorithm for the circuit above, with H as the Source S, and with N as the Target T. You can start with the path H->N.

   a.) List the paths that you use (i.e. for the first you would write “HN”).

   b.) Indicate which node is in which of the resulting partitions.

   c.) What is the cutsize?

2.) We are attempting to create a floorplan with the ILP algorithm. Assume there are two elements, A with area 60 and B with area 15. A has an aspect ratio between 0.6 to 3.75, while B must be of width 3, height 5. List ALL of the equations necessary to specify this floorplanning problem. Assume the F_width of the overall layout will be 12. Do not try to solve by hand – you must set up the equations that would be solved by the ILP algorithm.
3.) For the floorplan tree below, give the width and height for all possible floorplans, including specifying the orientation for A and B. Given that there are two orientations for both A and B, you will have 4 possible floorplans.